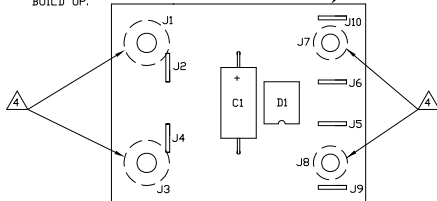


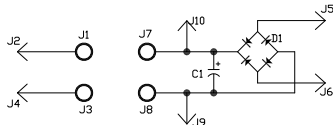
NOTES:

1. ALL COMPONENT SUBSTITUTIONS MUST BE ENGINEERING APPROVED BY CENTURY MFG. CO.
2. SOLDERING AND GENERAL WORKMANSHIP TO COMPLY WITH IPC-CM-770B.
3. BOARDS MUST BE PACKAGED IN A WAY THAT NO CIRCUIT BOARD WILL BE TOUCHING ANOTHER.

△ THIS AREA TO BE FLAT AND FREE OF SOLDER BUILD UP.



Digi-Key - 1-800-344-4539



SCHEMATIC DIAGRAM

TEST PROCEDURE:

1. CONNECT 16 VOLTS AC RMS TO TERMINALS J5 AND J6.
2. CONNECT A LOAD OF 65 OHMS $\pm 5\%$ (8 WATTS) TO TERMINALS J10 AND J9.
3. OBSERVE THE VOLTAGE AT TERMINALS J10 AND J9; VERIFYING THAT IT IS 21.25 ± 0.75 VOLTS DC. TERMINAL J10 TO BE POSITIVE POLARITY.

J6	1	FASTON TAB, .250 MALE
J5	1	FASTON TAB, .250 MALE
J4	1	FASTON TAB, .250 MALE
J3	1	FASTON TAB, .250 MALE
J2	1	FASTON TAB, .250 MALE
J1	1	FASTON TAB, .250 MALE
D1	1	DIODE, 1 AMP, 100 VOLT, DIGI-KEY P/N DB102-BPMS-ND
C1	1	CAPACITOR, 330 MFD, 25 VOLT ELECTROLYTIC. PANASONIC SU SERIES, AXIAL Digi-key #4043PHBK-ND
PCB1	1	PRINTED CIRCUIT BOARD, CENTURY P/N (510-113-001)
ITEM	QTY	DESCRIPTION

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SYM		REVISION	DATE	BY
DO NOT SCALE		CENTURY MFG CO		
UNLESS SPECIFIED		9231 PENN AVE SD		
DIMENSIONS ARE IN INCHES		MPLS, MN 55431		
TOLERANCES UNLESS SPECIFIED		OWN BY JAD	DATE 10-26-94	MAT'L
DECIMALS: ± 0.015		BFT CHK		
FRACTIONS: $\pm 1/64$		ENG CHK		
ANGLES: $\pm 0.5^\circ$		ENG APPV WPW	11-2-94	
PREVIOUSLY RELEASED AS APPV		MAT'L NO		
P. - REV. -		RELEASE 15015	DATE ISSUED	DEPT CODE
TITLE		CIRCUIT BOARD ASSY		
		880-318-001		